



Activity Report

IEEE SSCS Japan Chapter

ASSCC 2016

2016.11.7 @ Toyama

Chair: Takeshi Yamamura, Fujitsu Laboratories

Vice Chair: Makoto Ikeda, University of Tokyo

Secretary: Hiroyuki Ito, Tokyo Institute of Technology

Treasurer: Tetsuya Iizuka, University of Tokyo



Major Activities of SSCS Japan Chapter in 2016

- I. Sponsored Seminars : Co-sponsored with Kansai Chapter
 1. Review Seminar of ISSCC 2016
 2. Review Seminar of VLSI Circuits Symposium 2016
 3. Review Seminar of A-SSCC 2016 (to be held in November 11th, 2016)
including Lecture by Professor Asad Abidi of UCLA

- II. Co-sponsored Technical Meetings
 1. LSI and Systems Workshop 2016
Sponsored by Integrated Circuits and Devices Committee of IEICE
 2. Designers Forum
Sponsored by VDEC

- III. Sponsored Awards for Young Researchers
 1. IEEE SSCS Japan Chapter Academic Research Award
for the two best student posters at LSI and Systems Workshop 2016
 2. IEEE SSCS Japan Chapter VDEC Design Award
for the best student design at VDEC Designers Forum 2016



Review Seminar of ISSCC 2016

- Date&Place: February 22, 2016
- Place: Tokyo Institute of Technology
- Invited Talk by Takehiro Nakamura (Docomo)
 - “ 5G Technologies and Development in 2020 and Beyond”
- Presentations by the 14 Japanese authors of ISSCC 2016
- Number of Attendees: 100



Review Seminar of VLSI Circuits Symp. 2016

- Date&Place: June 22, 2016
- Place: Tokyo Institute of Technology
- Invited Talk by Prof. Asad Abidi (UCLA)
 - “Sampling Circuits: Unified treatment that describes sampling oscilloscope front-ends”
- Presentations by the 10 Japanese authors of VLSI Circuits 2016
- Number of Attendees: 76



Academic Research Award

- May 17, 2016@University of Tokyo, Komaba Campus
- Awarded researchers
 - #Hiroki Asano (Kobe University), “A fully integrated 1- μ s start-up time, 32-MHz relaxation oscillator for low-power intermittent systems”
 - #Toru Kawajiri (Keio University), “Dynamic period switching of PRS-PWM with run length limiting technique for spurious and ripple reduction in fast response wireless power transmission”

SSCS JC, VDEC Design Award

- August 25th, 2016@Univ. of Tokyo, Hongo Campus
- Awarded researcher
 - #Hayato Shiomi (Kyoto University)

VDEC Design Award (2nd Place)

- #M. Tamura (Univ. of Tokyo), A. Yoshimura (NAIST) and T. Asano (Kobe Univ.)



Other activities of SSCS Japan Chapter in 2016

I. Technocally co-sponsored events:

1. International Symp. On IoT Enabling Chips: June 20, 2016
Presentations: 7, Attendance: 124
2. Technical meeting on Analog RF: June 25, 2016
Presentations: 8, Attendance: 41
3. Technical meeting of IC: July 2-3, 2016
Presentations: 13, Attendance: 60
4. Technical meeting on Analog RF: July 20-23, 2016
Presentations: 1, Attendance: 20
5. VDEC 10th D2T Symposium
Presentations: 9, Attendance: 125
6. Technical meeting on IC: July 24-25, 2016
Presentations: 15, Attendance: 40
7. Technical meeting on IC: Oct. 26-27, 2016
Presentations: 13
8. Technical meeting on Analog RF: Nov. 24, 2016



Other topic on SSCS Japan Chapter

Structure change:

SSCS Japan Chapter was founded and located under Japan Council

→ Now located under Japan Section, covering all Japanese sections except Kansai section



Planned Activities for 2017

Review Seminar of ISSCC 2017, jointly organize with Kansai

Review Seminar of VLSI 2017

Review Seminar of A-SSCC 2017

Seminars by distinguished lecturers (at least 1)

Sponsoring technical meetings (at least 10)

Sponsoring awards for young researchers

1. IEEE SSCS Japan Chapter Academic Research Awards
at LSI and Systems Workshop
2. IEEE SSCS Japan Chapter VDEC Design Award
at VDEC Designers Forum

